

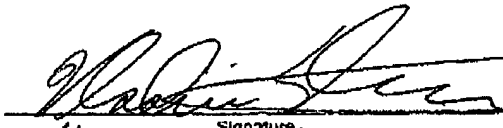
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PTO/SB/33 (07-05)
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PRE-APPEAL BRIEF REQUEST FOR REVIEW		Docket Number (Optional) P-6274-US	
I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to "Mail Stop AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)]		Application Number 10/774,806	Filed 02/10/2004
Signature _____		First Named Inventor Alexander Kushnarenko	
Typed or printed name _____		Art Unit 2816	Examiner TON, MY TRANG
Applicant requests review of the final rejection in the above-identified application. No amendments are being filed with this request.			
This request is being filed with a notice of appeal.			
The review is requested for the reason(s) stated on the attached sheet(s). Note: No more than five (5) pages may be provided.			
I am the		 Signature Vladimir Sherman Typed or printed name	
<input type="checkbox"/>	applicant/inventor.		
<input type="checkbox"/>	assignee of record of the entire interest. See 37 CFR 3.71. Statement under 37 CFR 3.73(b) is enclosed. (Form PTO/SB/98)		
<input checked="" type="checkbox"/>	attorney or agent of record. Registration number 43116	(212) 658-9933 Telephone number	
<input type="checkbox"/>	attorney or agent acting under 37 CFR 1.34. Registration number if acting under 37 CFR 1.34 _____	March 13, 2006 Date	
NOTE: Signatures of all the inventors or assignees of record of the entire interest or their representative(s) are required. Submit multiple forms if more than one signature is required, see below.			
<input type="checkbox"/> *Total of _____ forms are submitted.			

This collection of information is required by 35 U.S.C. 132. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11, 1.14 and 41.6. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Mail Stop AF, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Attorney Docket No.: P-6274-US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): KUSHMARENKO, Alexander Examiner: TON, MY TRANG
Serial No.: 10/774,806 Group Art Unit: 2816
Filed: October 2, 2004
Title: HIGH VOLTAGE LOW POWER DRIVER

**ARGUMENTS FILED IN SUPPORT OF
PRE-APPEAL BRIEF REQUEST FOR REVIEW**

Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Sir:

This communication is filed in response to the Office Action dated December 12th, 2005, issued by the United States Patent and Trademark Office in connection with the above-identified Application. A response to the Office Action was due March 12th, 2006, which is a Sunday. Accordingly, the response is due on March 13, 2006 which is the following working day, and this response filed on March 13, 2006 is being timely filed.

These below arguments are being filed in support of the attached: (1) Notice of Appeal, and (2) Pre-Appeal Brief Request For Review.

Arguments:

- (1) The Examiner objected to the drawing under 37 CFR 1.83(a) for allegedly not showing every feature of the invention specified in the claims, specifically the Examiner had stated that the limitation "a given voltage source", which is cited in claims 1 and 13, must be shown. Applicant kindly wishes to point out that the specified limitation appear in the Figures. The elements that indicate a

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given voltage source in Figures 1 and 2 (prior art) are HV, VCC and IN, in Figure 3. HV, LV and VCC indicate a given voltage source.

- (2) The Examiner's had rejected claims 1 and 13 under 35 U.S.C 112, second paragraph. The Examiner had stated that he believes that there is no support in the specification for the limitation "... a circuit path between a **given voltage source**..." which appears in claims 1 and 13.

Applicant kindly wishes to point out that the two limitations:

- **Claim 1 cites in part:** "...wherein circuitry is adapted to disconnect a circuit path between a given voltage source and said output node prior to connecting another voltage source..."
- **Claim 13 cites in part:** "...that a circuit path between a given voltage source and said output node is disconnected prior to connecting another voltage source..."

are supported throughout the application. Applicant wishes to specifically point out the following segments from the specification:

- **Page 8, paragraph 27:**

"High voltage source HV may be electrically connected to output node n_{OUT} via P_0 , e.g., by turning P_0 on, thereby raising the voltage at output node n_{OUT} to approximately high voltage HV; and low voltage source LV may be electrically disconnected from output node n_{OUT} , e.g., by turning N_2 off. This simultaneous action of switching from a low voltage pre-charge to a high voltage power source may avoid contention between the sources LV and HV."

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- **Page 9, paragraph 28:**

"In accordance with an exemplary embodiment of the present invention shown in Fig. 3, as shown in $t_3 - t_4$ in Fig. 4, when input IN_2 signal is switched OFF, e.g., returned to a high voltage level, and input signal IN_1 remains OFF, e.g., at a high voltage level, output node n_{OUT} may discharge through two paths, for example, through P_1 and N_2 to LV, and through N_0 and N_1 to ground. Thus, for example, so long as the voltage at n_{OUT} is higher than approximately the voltage of LV plus the threshold voltage of P_1 , n_{OUT} may discharge through P_1 and N_2 , as well as to ground through N_1 and N_2 . In this mode, a portion of the charge of output node n_{OUT} may optionally be returned to the low voltage source LV. "

- **Page 10, paragraph 33:**

"As indicated at $t_2 - t_3$ in Fig. 5, when input signal IN_2 is switched ON, IN_2 may open an electric path to the high voltage source HV, and P_0 may be turned on and electrically connect high voltage source HV to output node n_{OUT} , thereby raising the voltage at output node n_{OUT} to approximately high voltage HV. "

Applicant kindly wishes to point out that the term "...a circuit path between a given voltage source and said output..." is explained in the above cited references and so are the actions of "...disconnecting a path..." and "...connecting another voltage source..." and that the use of this actions are a well known to one of ordinary skills in the art.

Therefore, it would be appreciated that the added limitation to claims 1 and 13 are supported through out the application as filed. Applicant believes that to one of ordinary skills in the art, the specification disclosed in the application and specifically the above cited references, provide a full support to the added limitations.

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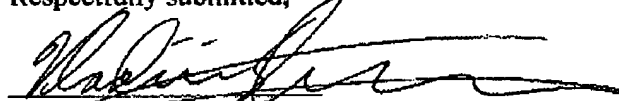
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(3) The Examiner's had rejected claims 1 and 13 under 102(b). The Examiner's rejection of claims 11 and 13 are based on the Examiner's mistaken belief that the following limitation are not supported in the specification:

- Claim 1 cites in part: "...wherein circuitry is adapted to disconnect a circuit path between a given voltage source and said output node prior to connecting another voltage source..."
- Claim 13 cites in part: "...that a circuit path between a given voltage source and said output node is disconnected prior to connecting another voltage source..."

The Examiner established his 102 rejections without taking the above limitation into consideration, and by that based his 102 rejections on a mistaken assumption concerning the present invention. Therefore applicant believes that this rejection should be withdrawn.

Respectfully submitted,



Vladimir Sherman

Attorney for Applicant(s)

Registration No. 43,116

Dated: March 13, 2006

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